

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

JEROEN A.J. LEIJTEN ET AL

NL 000681

Serial No.

Filed: CONCURRENTLY

SIGNAL PROCESSING DEVICE AND METHOD FOR SUPPLYING A SIGNAL
PROCESSING RESULT TO A PLURALITY OF REGISTERS

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

3. (Amended) A device according to claim 1, characterized in that said signal processing device is a programmable VLIW processor, and said register files are partitioned register files (RF1-RFn), wherein a data stationary instruction encoding is used.

4. (Amended) A device according to claim 1, characterized in that said corresponding indication is an information stating that said result is to be written to said register address of said selected register files.

5. (Amended) A device according to claim 1, characterized in that said corresponding indication is a result index (RI) which refers to a multicast or broadcast register in said selected register files.

6. (Amended) A device according to claim 1, characterized in that said register allocation means comprises demultiplexing means (DM1-DM3) for demultiplexing said result and said register address (RRI) to said selected register files in response to said corresponding indication.

7. (Amended) A device according to claim 1, characterized in that said functional units are functional unit clusters (UC1-UCn).

11. (Amended) A method according to claim 8, characterized in that said selection step comprises a demultiplexing step of demultiplexing said result and said register address to said selected register files in response to said corresponding indication.

REMARKS

The foregoing amendments to claims 3-7 and 11, were made solely to avoid filing the claims in the multiple dependent form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all rights he may have under the Doctrine of Equivalents. Applicant furthermore reserves his right to reintroduce

subject matter deleted herein at a later time during the
prosecution of this application or continuing applications.

Respectfully submitted,

By 

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10016134-131001

APPENDIX

3. A device according to claim 1 ~~or 2~~, characterized in that said signal processing device is a programmable VLIW processor, and said register files are partitioned register files (RF1-RFn), wherein a data stationary instruction encoding is used.
4. A device according to claim 1 ~~anyone of the preceding claims~~, characterized in that said corresponding indication is an information stating that said result is to be written to said register address of said selected register files.
5. A device according to claim 1 ~~anyone of claims 1 to 3~~, characterized in that said corresponding indication is a result index (RI) which refers to a multicast or broadcast register in said selected register files.
6. A device according to claim 1 ~~anyone of the preceding claims~~, characterized in that said register allocation means comprises demultiplexing means (DM1-DM3) for demultiplexing said result and said register address (RRI) to said selected register files in response to said corresponding indication.
7. A device according to claim 1 ~~anyone of the preceding claims~~, characterized in that said functional units are functional unit clusters (UC1-UCn).
11. A method according to claim 8 ~~anyone of claims 8 to 10~~, characterized in that said selection step comprises a demultiplexing step of demultiplexing said result and said

register address to said selected register files in response to said corresponding indication.

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